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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

ASA-1236

SERIAL NO.

10/533,127

APPLICANT

Y. KOMIYA, et al

FILING DATE

April 29, 2005

GROUP 2858

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
														YES
/VN/	0	6	2	8	1	7	0	0	10/94	Japan			XX	
/VN/	0	9	1	5	2	4	7	2	6/97	Japan			XX	
/VN/	1	1	0	4	4	7	2	4	2/99	Japan			XX	
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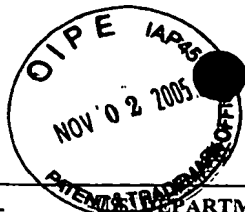
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER /Vincent Q. Nguyen/

DATE CONSIDERED 11/04/2007

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])



Form PTO-1449	DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. ASA-1236	SERIAL NO. 10/533,127
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT Yasumaro KOMIYA, et al	
		FILING DATE November 2, 2005	GROUP Not Yet Assigned 2858

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
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AH						
AI						
AJ						
AK						
AL						

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Abstract	
						Yes	No
/VN/	AM	2001-166012	06/01	JP		x	
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	AS						
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/VN/	AU	"CMOS Open Defect Detection by Supply Current Test" by Masaki HASHIZUME, et al., Proc. of IEEE International Workshop on Defect Based Testing, pp. 509-513, publ. March. 2001.
	AV	
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4/29/2005

JC12 Rec'd CT/PTC 29 APR 2005

Sheet 1 of 1

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. ASA-1236	SERIAL NO. 10/533127
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT Yasumaro KOMIYA, et al	
		FILING DATE April 29, 2005	GROUP

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Date Considered	11/05/2007	